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SURGE SUPPRESSION DEVICE

ABSTRACT OF THE DISCLOSURE

An apparatus and method for protecting hardware devices using a spiral inductor. The surge suppressor protects hardware devices from electric surges by isolating the radio frequency from an inner conductor. The surge suppressor includes a housing, an inner conductor, a surge blocking device, and a spiral inductor. The surge blocking device is inserted in series with the hardware devices for blocking the flow of electrical energy therethrough. The spiral inductor is coupled to the surge blocking device and is shunted to ground for discharging the electrical surge.

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